

# Architecture Review / Quality Gate #2 Completion Procedures

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This document attempts to define the procedures used by Sales and Marketing District Technical Solutions Consultant (TSC) to complete Quality Gate #2. These procedures were developed to satisfy all checklist items listed on the Quality Gate #2 form as follows:

1. Baseline architecture document signed-off by the Development and Production Services Organization (PSO) District Managers.
2. All issues brought up in the architecture review have been resolved to the satisfaction of the Development and PSO District Managers.
3. Architecture supports the availability, reliability, security, and performance requirements specified by the users.
4. Plans are in place to handle expected future growth.
5. All CIO Foundation Architecture deviations have been approved.

## **Step #1 – Obtain Vendor Baseline Architecture Review Document (BARD)**

This step is linked to the Acquisition Process by the Initiative Manager. In addition to the Initial Requirements Document (IRD), the TSC works with the customer to obtain a hardware diagram document. Optimally, this document contains the following definitions in either written descriptions or illustrations:

- 1.) Each cabinet (CPU, Tape, and DASD) and monitors (consoles) as included in the IRD.
- 2.) All internal PCB boards and their functionality within each cabinet.
- 3.) All cable connections between internal boards between each cabinet.
- 4.) All private and data center network connections (defined by the customer).
- 5.) All power connections within each cabinet.

Experience: Several vendors have initially objected to providing this document. However, when presented with the reality of the purchase price and value to the data center operations, they have typically conceded to provide a document that satisfies the requirements.

## **Step #2 – TSC Review Vendor BARD and IRD for Approval**

This step consists of reviewing the final version of the BARD and IRD for completeness and accuracy. If these documents do not meet these requirements, the customer is contacted to get the necessary information. If these documents are accepted, the TSC forwards the IRD to the Initiative Manager indicating that the Acquisition Process may commence.

Note: This approval step allows the TSC to ensure the documentation requirement for the BARD that will be used for the Quality Gate #2 Architecture Review with PSO and data center personnel are in compliance.

## **Step #3 – Architecture Review Meeting**

This step consists of holding a formal Architecture Review meeting with the customer, PSO, and data center personnel. The core PSO and data center representatives include: Technical Work Leads (TWL), Design Test Planners (DTP), and System Platform Administrators (SPA). On occasion, when necessary the team also includes the following representatives: Teradata Support representatives, Systems Level Analysts (SLA), Operations Managers, PSO Account Managers, and PSO Architects.

The TSC schedules the Architecture Review meeting in advance of the Project Implementation meetings in an attempt to complete Quality Gate #2 prior to the meetings. When the Architecture Review meeting date has been established, the TSC distributes the IRD and BARD to the core team members well in advance of the meeting to allow time to review each document. In the interim before and during the meeting, these representatives raise any and all issues discovered in the IRD or BARD for discussion.

Experience: Some issues brought up in the past have included the following (in random order):

- 1.) SC to MIC connector adapters are ordered or available (if necessary).
- 2.) Network redundancy requirements and specifications including subnets are defined.
- 3.) Sufficient processor capacity is available during full (100%) CPU utilization for OA&M tools execution.
- 4.) Specific application backup procedures and windows are sufficiently defined.
- 5.) System redundancy requirements such as NCR Life-Keeper and HP Service Guard are defined.
- 6.) Implementation plans and schedules for piece part installations are documented.
- 7.) Single points of failures and their acceptance are documented.
- 8.) AutoRAID usable disk space configuration with constant space requirements is defined.
- 9.) Description of Ethernet port functionality when used in conjunction with the console port.
- 10.) Descriptions of Ethernet failover hardware and software features including hub failover.

After all issues are raised, the core team assigns ownership to resolve all issues. Once resolved, the TSC solicits the first four Quality Gate #2 checklist items approval from each team member. If a team member does not approve of any checklist items, the team revisits the issues until resolved. If the team members approve all four checklist items, the TSC affirms Quality Gate #2 complete, fills out the Quality Gate #2 checklist document, and forwards the document to the Development District Manager and the PSO District Manager for electronic signature.

Note: The TSC does not obtain approval for Quality Gate #2 checklist item number 5 because this is the sole responsibility of the TSC.

After the TSC receives the signed Quality Gate #2 document from both organizations, the TSC updates the DBOR application with the Quality Gate #2 completion status record.